

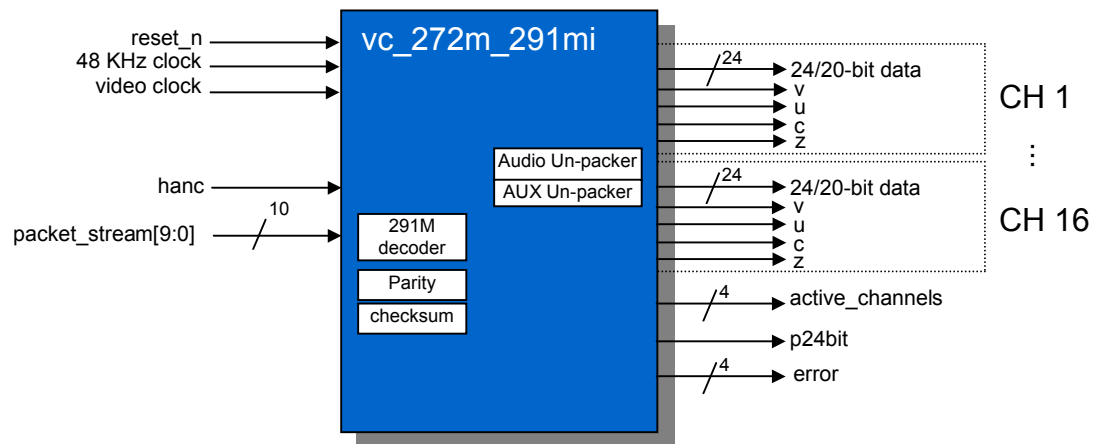


### SMPTE 272M/291M Decoder: vc\_272m\_291mi

This Core Values™ decoder allows designs to extract up to sixteen 48 KHz 24-bit digital audio channels from the Horizontal Ancillary (HANC) area of an ITU-R 656 10-bit parallel stream.

#### FEATURES

- Decodes SMPTE 272M (HD Embedded Audio)
- Extracts up to 16 AES/EBU 48 KHz channels
- Decodes SMPTE 291M packets
- Detects Parity and Checksum errors
- Accepts 525-line and 625-line streams
- 24-bit or 20-bit Audio
- Detects SMPTE 291M deviations
- Linted and Verified Verilog 2001 Source Code
- Permanent Company License (not per-project)
- Targeting ASICs and Actel, Altera, Lattice and Xilinx FPGAs

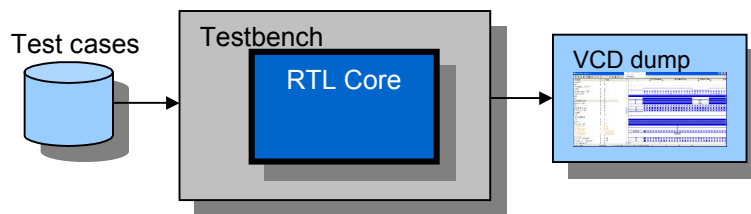


#### EXTERNAL REQUIREMENTS

- 48 KHz clock synchronized with video clock
- ITU-R 656 decoder

#### DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- VCD Functional Dump
- Fast Verilog 2001 Testbench and Test cases
- User Manual



#### APPLICATIONS

- Professional Recording Equipment
- Broadcast Compression Equipment
- Professional Audio Processing Equipment
- Prosumer Electronics with Digital Embedded A/V



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