

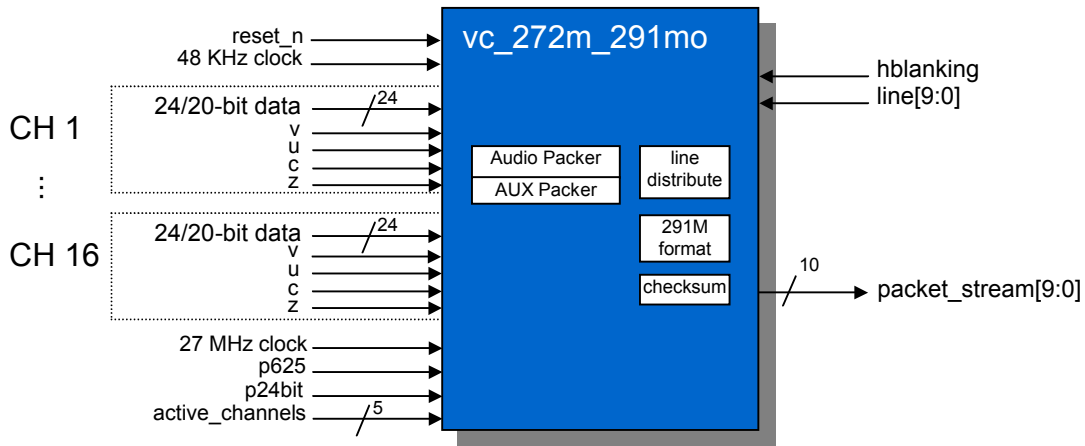


SMPTE 272M/291M Encoder: vc_272m_291mo

This Core Values™ encoder allows designs with up to sixteen 48 KHz 20-bit or twelve 24-bit digital audio channels to embed the audio in the Horizontal Ancillary (HANC) area of an ITU-R 656-4 10-bit parallel stream.

FEATURES

- Encodes SMPTE 272M-AC (SD Embedded Audio)
- Receives up to sixteen AES 48 KHz channels
- Creates SMPTE 291M packets for embedding
- Accepts hblanking signal and line count
- 24-bit (12 channels) or 20-bit (16 channels) Audio
- Encodes 525 and 625 line video
- Linted and Verified Verilog 2001 Source Code
- Permanent Company License (not per-project)
- Targeting ASICs and Actel, Altera, Lattice and Xilinx FPGAs

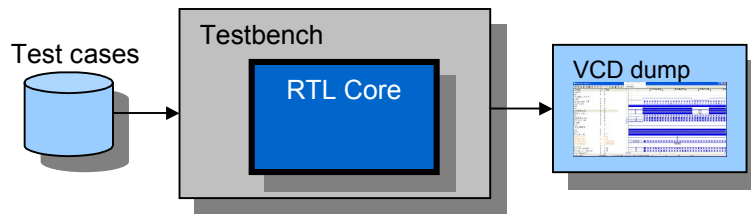


EXTERNAL REQUIREMENTS

- 48 KHz clock synchronized with video clock
- ITU-R 656-4 encoder signaling

DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- Fast Verilog 2001 Testbench and Test cases
- VCD Functional Dump
- User Manual



APPLICATIONS

- Professional Television Cameras
- Professional Audio Processing Equipment
- Professional Recording Equipment
- Prosumer Electronics with Digital Embedded A/V