

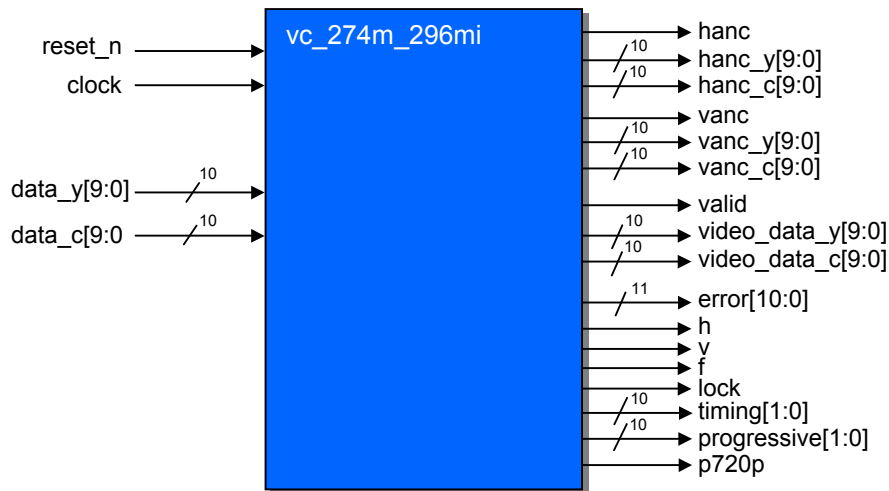


SMPTE 274M/296M Decoder: vc_274m_296mi

This Core Values™ encoder allows designs to receive SMPTE 274M/296M 74.25 MHz and 74.25/1.001 MHz 20-bit data with embedded syncs.

FEATURES

- Decodes HD SMPTE 274M (1080i/p), 296M (720p)
- Detects 24, 50 Hz and 59.94/60 Hz timings
- Produces video-and-data-plus-sync to external logic
- Extracts H, V, F, HANC and VANC ancillary data
- Detects 22 different errors in embedded stream
- Detects 1080i/p and 720p with Lock signal
- Linted Verilog 2001 Source Code
- Permanent Company License (not per-project)
- Targeting ASICs and Actel, Altera, Lattice and Xilinx FPGAs

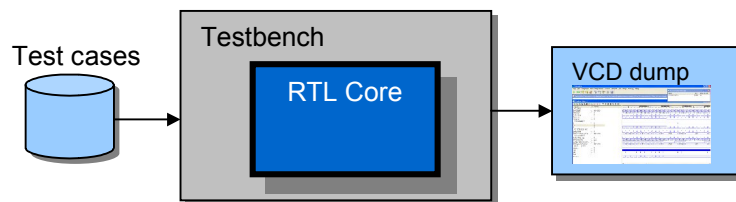


EXTERNAL REQUIREMENTS

- Active-low reset and 74.25 or 74.25/1.001 MHz clock
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DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- Verilog 2001 Testbench and Test cases
- VCD Functional Dump
- User Manual



APPLICATIONS

- Professional Audio/Video Transmission
- Professional Video Processing
- Prosumer High-definition Devices
- Rack-mounted Media Systems