

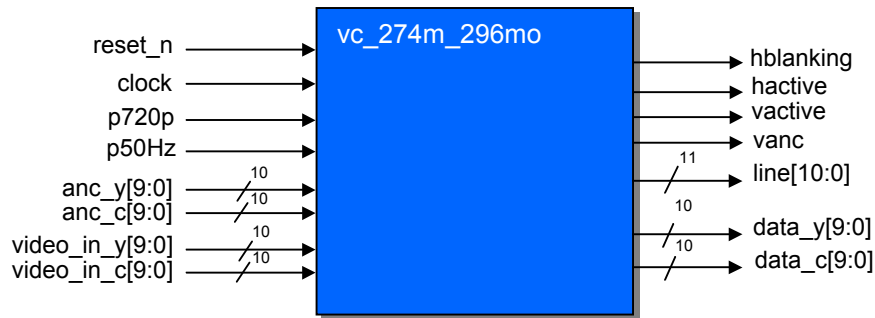


## SMPTE 274M/296M Encoder: vc\_274m\_296mo

This Core Values™ encoder allows designs with internal 1080i or 720p 4:2:2 YCbCr High Definition 20-bit video and data buses to connect to devices that receive SMPTE 274M/296M 74.25 MHz and 74.25/1.001 MHz 20-bit data with embedded syncs.

### FEATURES

- Encodes to SMPTE 274M/296M, HD 1080i/720p
- Supports 50 Hz, 59.94 Hz and 60 Hz
- Acts as master to video-and-data-plus-sync logic
- Signals VANC ancillary data period for data insertion
- Signals HANC ancillary data period with HBLANKING
- Clamps active video to valid non-sync values
- Linted Verilog 2001 Source Code
- Permanent Company License (not per-project)
- Targeting ASICs and Actel, Altera, Lattice and Xilinx FPGAs

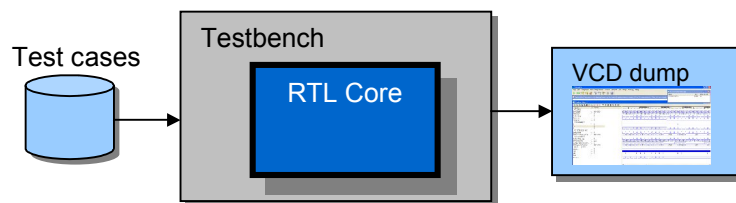


### EXTERNAL REQUIREMENTS

- 74.25 or 74.25/1.001 MHz clock

### DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- Verilog 2001 Testbench and Test cases
- VCD Functional Dump
- User Manual



### APPLICATIONS

- Professional Audio/Video Transmission
- Professional Video Processing
- Prosumer High-definition Devices
- Rack-mounted Media Systems