

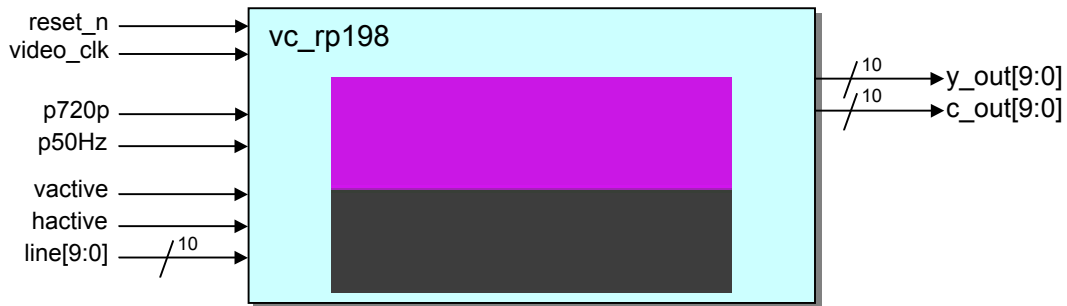


SMPTE RP198 10-bit HD SDI Pathological Pattern: vc_rp198

This Core Values™ product implements the SMPTE RP198 HD-SDI 720p and 1080i pathological pattern for SMPTE 292M transmitters, cables and receivers.

FEATURES

- Simple two-bit control interface
- Glue-less interface to SMPTE 274M and 296M cores
- 10-bit Y and C 4:2:2 Outputs
- Linted and Verified Verilog 2001 Source Code
- Permanent Company License (not per-project)
- ASICs and Actel, Altera, Lattice and Xilinx FPGAs

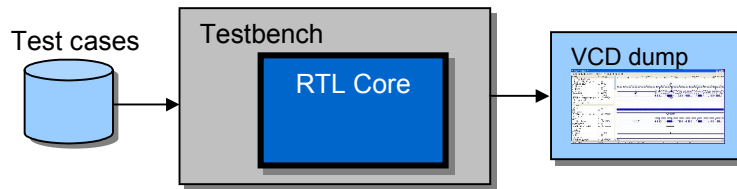


EXTERNAL REQUIREMENTS

- Reset, Input Video Clock

DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- VCD Functional Dump
- Fast Verilog 2001 Testbench and Test cases
- User Manual



APPLICATIONS

- Broadcast Video Cameras
- Professional Video Cameras
- Consumer Electronics Video Processing
- Professional Pattern Generators