

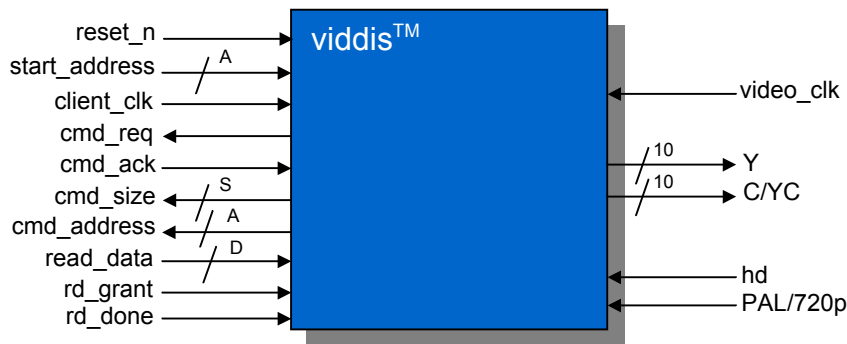


Video Display: viddis™

This Core Values™ product reads High Definition (HD) or Standard Definition (SD) video from memory for display or processing operations.

FEATURES

- Connects directly as a sdramio™ memory client
- Outputs 20-bit or 10-bit 4:2:2 YCbCr data
- Programmable burst size of 8 or 32 words
- Customized FIFO depth depending on bandwidth
- Produces SMPTE 274M/296M and ITU-R 656-4
- Linted and Verified Verilog 2001 Source Code
- Permanent Company License (not per-project)
- ASICs and Actel, Altera, Lattice and Xilinx FPGAs

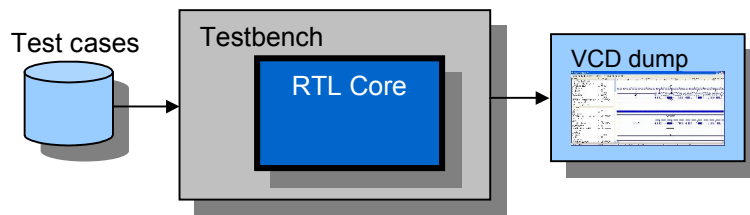


EXTERNAL REQUIREMENTS

- Active-low reset, Video Clock, Memory Client Clock
-

DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- Fast Verilog 2001 Testbench and Test cases
- VCD Functional Dump
- User Manual



APPLICATIONS

- Televisions
- Video Processing Equipment
- DVRs
- Security Cameras